



Netlist and Schematic Capture

Accelerate PCB Repair & Diagnostics with Automated Schematic Generation

Reverse-engineer. Diagnose. Document.

Unlock the power of Astronics' TestVue software and the PinPoint system to rapidly generate accurate circuit schematics — no original design files needed.

Define the Board – Digitize Your Layout

Easily create a digital representation of your physical board layout using TestVue's intuitive graphical interface. Assign device types to each component and the software automatically maps them to their corresponding symbols — setting the foundation for seamless schematic generation.

Capture NetLists – Safe, Power-Free Connection Mapping

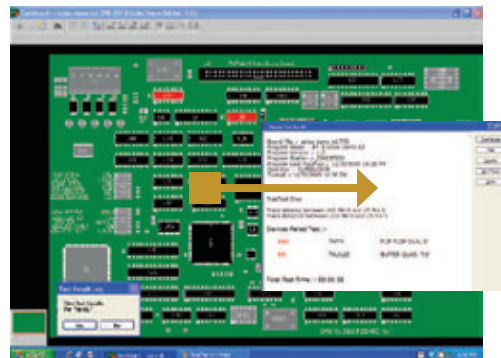
TestVue's built-in NetList Generator captures connections between devices using resistance-based measurement — no power required, making it safe for sensitive circuits. Simultaneously access

multiple components to reduce time and boost efficiency.

Generate Schematics – Instantly & Accurately

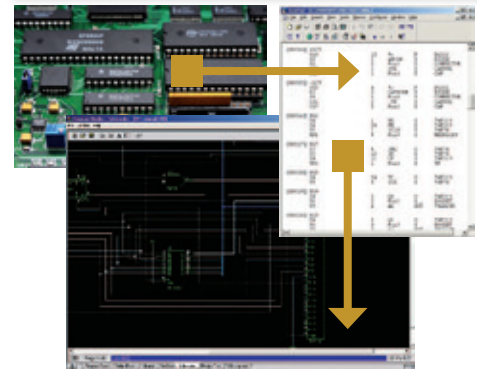
With the NetList and device symbols in place, TestVue automatically generates clean, readable schematics — viewable on-screen or exportable in industry-standard EDIF formats. Highlights include:

- Rapid generation in seconds
- Auto-creation of unknown symbols
- Multi-page navigation with net labeling
- Bus creation and hard copy print support



KEY FEATURES

- Multi-device access for speed
- Programmable resistance (1–50 Ω)
- Design Rule Checking
- Custom symbol library & CAD import
- User-defined execution order



Get Started Today

For additional details, please contact Astronics Test Systems.

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